(54) Title: A RECONFIGURABLE STATE MACHINE ARCHITECTURE AND RELATED METHOD OF EXECUTION
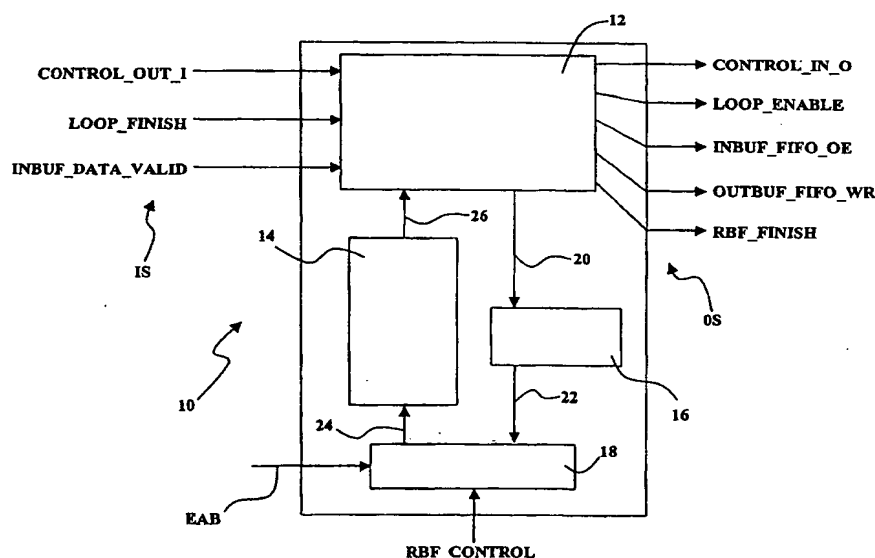
(57) Abstract: An architecture for a state machine (10) with a number of states of the machine, comprises a memory (14) having a set of addresses. The memory (14) is arranged to store at each of said addresses in the set the complete description of a respective one of said number of states of the machine (10).